SEP 3 0 2002 E

PATEDAT Docket Ma.: 200951/147

Examiner:

Que T. Le

Art Unit: **2878**

N THE UNITED STATES PATENT AND TRADEMARK OFFIČE O

Applicants

Jeffrey Zarnowski, Matthew Pace,

Thomas Vogelsong, and Michael Joyner

Serial No.

09/490,374

Cnfrm. No.

7076

Filed

January 24, 2000

For

A VIDEO BUS FOR HIGH SPEED

MULTI-RESOLUTION IMAGERS

PRELIMINARY AMENDMENT

U.S. Patent and Trademark Office

P.O. Box 2327

Arlington, VA 22202

Box: RCE

Dear Sir:

In response to the Office Action dated April 1, 2002, please amend the above-identified patent application as follows:

IN THE SPECIFICATION:

Please replace the paragraph beginning at page 5, line 1 with the following

paragraph:

The active pixel 50 of the prior art includes a photogate 60 and a transfer gate 62 that are used to couple photo generated charge onto the floating diffusion node 52 which is connected to the gate 56 of source follower 53. The drain of the output FET 53 is connected directly to a power supply rail VDD. The source follower output FET is in turn connected to the source 57 of row access FET 58. When the row access FET 58 is selected for reading, the



B